

Appl. No. 10/783,491
Response Dated September 15, 2008
Reply to Office action of May 14, 2008

STATUS OF CLAIMS

Claims 8 – 10, 16, 17, 19, 25, 27, 28 and 30-34 are pending.

Claims 19 was objected to.

Claims 8 – 10, 16, 17, 25, 27, 28 and 30-34 were rejected.

In view of the Examiner's Interview of September 10, 2008, all claim rejections have been overcome and the present claims are in condition for allowance.

REMARKS

Examiner Interview Summary

Applicants thank the Examiner for the courtesy of a telephonic interview on September 10, 2008. The Applicants further acknowledge that agreement was reached with the Examiner on September 10, 2008, that the present Office Action is hereby withdrawn and that all claims in present form are allowable. This agreement was reached based on the interview conducted on September 10, 2008, the substance of which Applicants refer to in the Agenda Items for Examiner Interview dated August 8, 2008 (postponed by the Examiner until September 10, 2008) and attached hereto.

In view of the Examiner's telephone call to Applicant's attorneys on September 15, 2008 requesting a response to non-final Office Action be submitted to the Patent Office before a notice of allowance may be mailed out, the present response is provided.

35 U.S.C. 102(b) Rejections

Claims 8 – 10, 16, 17, 25, 27, 28 and 30-34 stood rejected under 35 USC 102(b) as being anticipated by US Patent 5,483,190 to McGivern. In view of the Examiner's interview conducted on September 10, 2008 this rejection is respectfully traversed. Claim 8 recites an interface for a monitor and a temperature probe which includes a temperature sensor. Other independent claims recite various other features relating to the structure associated with a temperature probe and method of modifying a temperature sensed from the temperature probe. In contrast, the cited reference McGivern discloses an electronic circuit which replaces a temperature probe by simulating the resistance characteristics of a temperature sensor. (Col. 2, lines 6 – 9). In McGivern, the electronic circuit disclosed is a simulation circuit that provides a wide range of resistance values at its output. McGivern simply discloses a means such as a microprocessor which provides a control voltage signal. (Col. 3, lines 50 – 52). McGivern does not disclose any means which is connected to an actual temperature sensor and which determines a modified resistive output based on the output of the temperature sensor. Furthermore, in McGivern, the control voltage signal is not dependent on any actual output of a temperature sensor; rather, the control voltage signal is generated randomly between a pre-determined range, e.g. 0 VDC to 10 VDC. Still further, McGivern does not teach or suggest the use of any predictive or correlative algorithm. In fact, McGivern

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does not need any predictive or correlative algorithm because the output of its circuit does not depend on any measured temperature by a temperature sensor.

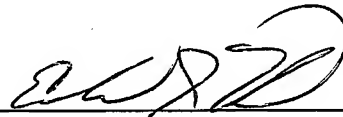
In view of the foregoing, and in view of the fact that the Examiner has agreed that this rejection should be withdrawn and that the application is in condition for allowance, said allowance is respectfully requested.

CONCLUSION

Having addressed all outstanding grounds raised by the Examiner, Applicants respectfully submit the present case is in condition for allowance, early notification of which is earnestly solicited.

Should there be any questions or outstanding matters, the Examiner is cordially invited and requested to contact Applicants' undersigned attorney at his number listed below.

Respectfully submitted,



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